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2021MODELS: 17" Disk Traction 20" Disk Traction 17"  
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Machine. This Manual Will Allow The Operator To Get  
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Premium - ELECTRIC GENERATOR Premium -  
GÈNÈRATRICE ...Premium Pulse/Sport - ELECTRIC  
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K9K1216U0C K9K1208D0C K9K1208U0C K9K1208Q0C  
K9K1216Q0C Document Title 64M X 8 Bit , 32M X 16  
Bit NAND Flash Memory Revision History The Attached  
Datasheets Are Prepared And Approved By SAMSUNG  
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The Right To Change The Specifications. 15th, 2021.  
16M-BIT [x 1 / X 2] CMOS SERIAL FLASH 32M-BIT [x 1 /  
X 2 ...The MX25L1605D Are 16,777,216 Bit Serial Flash  
Memory, Which Is Configured As 2,097,152 X 8  
Internally. When It Is In Two I/O Read Mode, The  
Structure Becomes 8,388,608 Bits X 2. The  
MX25L3205D Are 33,554,432 Bit Serial Flash Memory,  
Which Is Configured As 4,194,304 X 8 Internally. When  
It Is In Two I/O Read Mode, The Structure Be- 10th,  
2021Xtrinsic MMA8452Q 3-Axis, 12-bit/8-bit Digital  
Accelerometer 12-bit/8-bit Digital Accelerometer The  
MMA8452Q Is A Smart, Low-power, Three-axis,  
Capacitive, Micromachined Accelerometer With 12 Bits  
Of Resolution. This Accelerometer Is Packed With  
Embedded Functions With Flexible User Programmable  
Options, Configurable To Two Interrupt Pins.  
Embedded Interrupt Functions Allow For Overall Power  
Savings 10th, 20213x 16-bit Timer/Counter • 16-bit  
Low Energy Timer Ultra ...As A Wake-up Interrupt  
Controller Handling Interrupts Triggered While The CPU  
Is Asleep. The EFM32 Implementation Of The Cortex-  
M3 Is Described In Detail In EFM32G Cortex-M3  
Reference Manual. 2.1.2 Debug Interface (DBG) This

Device Includes Hardware Debug Support 17th, 2021.  
16-bit Pulse Counter • 16-bit Low Energy Timer LQFP48  
...The EFM32 Implementation Of The Cortex-M3 Is  
Described In Detail In EFM32 Cortex-M3 Reference  
Manual. 2.1.2 Debug Interface (DBG) This Device  
Includes Hardware Debug Support 7th, 202116-bit Low  
Energy Timer • 2× 16-bit Timer/Counter Ultra ...As A  
Wake-up Interrupt Controller Handling Interrupts  
Triggered While The CPU Is Asleep. The EFM32  
Implementation Of The Cortex-M3 Is Described In  
Detail In EFM32G Cortex-M3 Reference Manual. 2.1.2  
Debug Interface (DBG) This Device Includes Hardware  
Debug Support 1th, 2021High-Performance, 2-Ch,  
24-Bit, 216kHz Sampling Multi-Bit ...High-Performance,  
Two-Channel, 24-Bit, 216kHz Sampling Multi-Bit Delta-  
Sigma Analog-to-Digital Converter • Digital High-Pass  
Filtering Removes DC Offset 23 4 • Supports Linear  
PCM Output Data - Left And Right Channel Filters May  
Be - Output Sampling Rates From 8kHz To D 5th, 2021.  
LTC2666 - Octal 16-Bit/12-Bit  $\pm 10V$  Vout SoftSpan  
DACs With ...The LTC®2666 Is A Family Of 8-channel,  
16-/12-bit  $\pm 10V$  Digital-to-analog Converters With  
Integrated Precision References. They Are Guaranteed  
Monotonic And Have Built-in Rail-to-rail Output Buffers.  
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To  $\pm 10V$ . The Range Of Each Channel Is Independently  
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